

Notice of References Cited	Application/Control No.	Applicant(s)/Patent Under Reexamination	
	10/710,001	MO ET AL.	
	Examiner	Art Unit	Page 1 of 1
Stanetta D. Isaac	2812		

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,653,201	11-2003	Chung, Yi Sun	438/424
	B	US-6,798,038	09-2004	Sato et al.	257/510
	C	US-6,713,385	03-2004	Pipes et al.	438/637
	D	US-5,902,127	05-1999	Park, Tai-su	438/435
	E	US-6,531,377	03-2003	Knorr et al.	438/435
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Stanley Wolf, Silicon Processing For The VLSI Era, 1986, Lattice Press, Vol. II, pgs. 66-67
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.